Frequency Triplexer for Ultra-Wideband Systems (6–9 GHz)

Magnus Karlsson, Adriana Serban, Joakim Östh, Owais Owais and Shaofang Gong

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Frequency Triplexer for Ultra-wideband Systems (6-9 GHz)

Magnus Karlsson, Adriana Serban, Joakim Östh, Owais, and Shaofang Gong, Member, IEEE

Abstract—A triplexer for ultra-wideband radio implemented entirely with distributed microstrips is presented. The triplexer is composed of a triplex junction network and three bandpass filters. The circuit is integrated in a flex-rigid printed circuit board. Three flat 1 GHz sub-bands in the frequency band 6-9 GHz have been achieved. The group delay variation within each 1 GHz sub-band was measured to be less than 0.4 ns. A good agreement between simulation and measurement was obtained, e.g., less than 0.3 dB difference in the forward transmission in the majority of the frequency band.

Index Terms—Bandpass filter, Broadside-coupled, Edge-coupled, Frequency multiplexing, Triplexer, UWB

I. INTRODUCTION

The principle of frequency multiplexing, i.e., to combine the spectra from several ports into one wide spectrum has previously been proposed in a number of ways [1]-[12]. Suggestions about how to implement multiplexing or pre-selecting techniques in ultra-wideband (UWB) systems have been made in [13]-[24]. The interest for high speed and short range wireless-communication has been one of the major driving forces behind the UWB development [25]-[28]. In earlier time the band 3.1-4.8 GHz, was the primary frequency-band used for UWB. Later on legal respiratory around the earlier time the band 3.1-4.8 GHz, was the primary frequency-driving forces behind the UWB development [25]-[28]. As a result the frequency-bands 6-8.5, 6-9, and 6-10.6 GHz have lately received an increased attention [30] and [31]. We have recently proposed a frequency multiplexing technique in our study on ultra-wideband antennas [20], transceivers [21] and [22]. A study of a multiplexer using planar microstrips was conducted in [23].

Frequency multiplexing is a technique that has been studied for decades and many implementations have been demonstrated using various techniques [1], [23]. For instance, references [2]-[3] present a generalized form of the manifold theory. Reference [4] presents an optimization algorithm for waveguide multiplexers. Reference [7] demonstrates methods for reducing the need of experimental correction when using computer-aided design. Reference [8] presents a lumped element and manifold microwave multiplexer using the high-temperature superconductor technology. Multiplexer design using printed circuit boards typically involves some lumped components, as in [10] both a duplexer and a triplexer are presented. Paper [13] proposes a stepped-impedance multiplexer for UWB and wireless local area network (WLAN) coexistence. In [14], a narrow-band four-channel multiplexer using open loop resonators for multi-band on-off keying UWB is demonstrated. Papers [15]-[16] show two channel-select multiplexers intended for UWB local oscillator signal selection, i.e., only one sub-band is active at the time. Some recent achievements are for instance a multiplexer with four narrow channels that are well separated (large guard band between the channels) is presented in [18]. Moreover, enhancement techniques in waveguide multiplexers due to optimization of the distance between channel filters placement [19]. Microstrip and stripline filters have been presented in many shapes and realized with various resonators, e.g., edge coupled and broadside coupled microstrips [23], [35] and [36]. One of the most common applications for a multiplexing circuit is a duplexer connecting Rx and Tx to the same antenna [37]. To achieve a good isolation between Rx and Tx can be achieved with sufficient channel spacing, as in the dual band impulse UWB radio proposed in [38]. A system with two channels centered at 3.5 and 4.5 GHz, respectively [38]. It is clear that also state of the art communication systems benefits from multi channel capability [37]-[39]. Hence, with an adequate triplexer a system capable of handling three high performance well isolated channels is possible.

Our previous paper [20] proposes antenna parallelism using frequency multiplexing. Our second paper [21] on the subject suggests using the multiplexer from [20] as pre-select filter for a UWB low-noise amplifier (3.1-4.8 GHz). In our third paper [22] the maximum theoretical data-rate limits of the proposed 11 sub-channel system using multiplexing techniques to combine the channels are studied. In our fourth paper [23] a multiplexer solution with three 500 MHz sub-bands implemented on a conventional printed circuit board was presented. Moreover, the idea of using the triplexer presented in [23] for parallel UWB channels was discussed in [24]. The triplexer in this paper is intended for 6-9 GHz UWB using a flex-rigid printed circuit board. Flexible materials have been used in wired interconnects, i.e., mainly in high speed interconnects in the Gbps domain [39]. Using the flex-rigid technology an antenna can easily be integrated in the flexible part of the printed circuit board while other components can be integrated in the rigid part [40]. One of the uniqueness’s

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Magnus Karlsson; email: magka@itn.liu.se, et al., are with Linköping University, Sweden.
with the design presented in this paper is that the triplexer is fully integrated in the “rigid part” of the flex-rigid printed circuit board using microstrips, resulting in a high performance and flexible solution but at low cost. Moreover, a detailed substrate stack was used to obtain good agreement between simulated and measured results. Most frequency multiplexing techniques published in the literature have used either waveguides or hybrids, having inherent difficulty for system integration and miniaturization. The flex-rigid substrate used in this study allows full integration of the radio front-end including the antenna [40]. One of the major challenges dealt with in this paper is that the sub-bands are side by side, i.e., virtually no guard-band. A typical guard-band normally has a relative bandwidth of 10% [23]. However, if such a large guard-band were to be used, too much frequency capacity would be lost. Hence, if only considering the two guard-bands in between the three sub-bands 1.5 GHz out of the 3 GHz would not be useable for data transmission.

II. OVERVIEW OF THE SYSTEM

All prototypes were manufactured using a flex-rigid printed circuit board with four metal-layer in the rigid part. The base substrate material is Pyralux polyimide from Dupont, processed together with a prepregs, as shown in Fig. 1. Prepreg is a sheet material (e.g., glass fabric) impregnated with a resin cured to an intermediate stage, ready for one stage printed circuit board bonding.

Table 1. Printed circuit board parameters

<table>
<thead>
<tr>
<th>Parameter (Polyimide)</th>
<th>Dimension</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dielectric constant</td>
<td>3.40±0.05</td>
</tr>
<tr>
<td>Dissipation factor</td>
<td>0.004</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Parameter (Adhesive)</th>
<th>Dimension</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dielectric constant</td>
<td>3.60±0.10</td>
</tr>
<tr>
<td>Dissipation factor</td>
<td>0.020</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Parameter (Metal, common)</th>
<th>Dimension</th>
</tr>
</thead>
<tbody>
<tr>
<td>Metal thickness, layer 1, 4</td>
<td>0.038 mm</td>
</tr>
<tr>
<td>Metal thickness, layer 2, 3</td>
<td>0.018 mm</td>
</tr>
<tr>
<td>Metal conductivity</td>
<td>5.8x10⁷ S/m (Copper)</td>
</tr>
<tr>
<td>Surface roughness</td>
<td>0.001 mm</td>
</tr>
</tbody>
</table>

Table 1 lists the printed circuit board parameters, with the stack of the printed circuit board layers shown in Fig. 1. Metal layers 1 and 4 are thicker than metal layers 2 and 3 because the surface layers are plated, when plating the VIAs of the printed circuit board. Only drilled through hole VIAs were used. No signal VIAs are needed in the design due to the utilization of broadside coupling. However, a number of ground VIAs were used to ensure a low impedance ground connection throughout the desired frequency band (Clearly seen later in Fig. 2, in section II-A).

Table 2 lists the detailed specification of the flex-rigid printed circuit board build-up shown in Fig. 1. It is seen in Tables 1 and 2 that the substrate height between Metal 1 and 2 and between Metal 3 and 4 is 0.1524 mm, whereas the substrate height between Metal 2 and 3 is 0.1270 mm.

### A. Triplexer

Fig. 2(a) shows the schematic of the proposed triplexer. The network is realized with a microstrip technology using the manifold principle of operation. The triplexer consists of three series quarter-wavelength transmission lines, three bandpass filters, and three transmission lines for tuning of the filter impedance. The principle of operation is that each sub-band has a matched input impedance (50 Ω in this design) at the junction, but a high input impedance at the input of other sub-bands. The series transmission lines of three different quarter wavelengths provide a high impedance at the respective frequency band, e.g., preventing sub-band #1 signals to reach sub-band #2 and #3. Ideally, the series transmission provides an admittance inversion at each passband, i.e., by rotating a quarter of a wavelength from short to open. Using a short as
the reference the series transmission lines then the ideal length is \( n^* \lambda /2 + \lambda /4 \) (n=0, 1, 2,..), e.g., n=0 that gives a shortest possible line-length of \( \lambda /4 \). The filter tuning lines at the junctions optimize the stop band impedance of each filter, to provide a high stop band impedance in the neighboring bands. Neighboring sub-bands are mostly critical to a sub-band since the band-pass filter has limited rejection close to its pass-band. The entire network is optimized together with the filters to achieve flat passbands and a symmetric performance between the sub-bands. Moreover, in this design a \( 3/2 \lambda \) (n=1, electrical length = 270°) rotation was used to achieve a sharper cut-off. Table 3 lists the specifications of the triplexer filters, where \( \text{length} \) is the length of the filter section, \( \text{width metal 1 and 2} \) are the widths of the layer 1 and 2 conductors in the broadside-coupled filter section, \( \text{conductor width} \) is the width of the edge-coupled filter section conductors, and \( \text{spacing} \) is the distance between the conductors. Note that the filters are symmetric, i.e., filter section 1 and 5 are equal, whereas 2 and 4 are also equal.

Table 3. Combined broadside- and edge-coupled filter specifications

<table>
<thead>
<tr>
<th>Filter Parameter</th>
<th>Sub-band #1</th>
<th>Sub-band #2</th>
<th>Sub-band #3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Broadside-coupled filter section (1 and 5)</td>
<td>6.34</td>
<td>5.42</td>
<td>4.77</td>
</tr>
<tr>
<td>Length (mm)</td>
<td>0.70</td>
<td>0.70</td>
<td>0.70</td>
</tr>
<tr>
<td>Width, metal 1 (mm)</td>
<td>0.35</td>
<td>0.35</td>
<td>0.35</td>
</tr>
<tr>
<td>Edge-coupled filter section (2 and 4)</td>
<td>6.34</td>
<td>5.42</td>
<td>4.80</td>
</tr>
<tr>
<td>Length (mm)</td>
<td>0.27</td>
<td>0.27</td>
<td>0.27</td>
</tr>
<tr>
<td>Conductor width (mm)</td>
<td>0.10</td>
<td>0.10</td>
<td>0.10</td>
</tr>
<tr>
<td>Spacing (mm)</td>
<td>0.10</td>
<td>0.10</td>
<td>0.10</td>
</tr>
<tr>
<td>Edge-coupled filter section (3)</td>
<td>6.31</td>
<td>5.39</td>
<td>4.77</td>
</tr>
<tr>
<td>Length (mm)</td>
<td>0.28</td>
<td>0.28</td>
<td>0.28</td>
</tr>
<tr>
<td>Conductor width (mm)</td>
<td>0.14</td>
<td>0.14</td>
<td>0.14</td>
</tr>
</tbody>
</table>

Fig. 2(b) shows a photo of the implementation prototype. The size of the triplexer prototype is 48 x 25 mm. The four edge mounted Sub-Miniature A (SMA) connectors are seen. Port 1 is the input/output (6-9 GHz), Port 2 is for sub-band #1 (6-7 GHz), Port 3 is for sub-band #2 (7-8 GHz), and Port 4 is for sub-band #3 (8-9 GHz).

B. Broadside- and edge-coupled filter

Fig. 3 shows how the combined broadside- and edge-coupled filter technology is implemented in the flex-rigid printed circuit board. The start and the stop segments are placed on Metal layer 1, and the rest of the filter is placed on Metal layer 2. A bandpass filter with five coupled sections, two orders from broadside-coupling and three orders from edge-coupling, are utilized. The filter was designed for a flat passband response.

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(a) Principle of the triplexer.

(b) Photo of the implementation.

Fig. 2. Triplexer: (a) principle of the triplexer, and (b) photo of the implementation prototype.

Fig. 3. Combined broadside- and edge-coupled filter.
III. THEORETICAL ANALYSIS

The triplexer circuitry presented in this paper operates according to the Manifold principle [2], [3] and [12]. Equation (1)–(6) describes the principle of operation illustrated in Fig. 4, starting at the ground reference and moving upwards along the vertical transmission-lines (d_{mn}). From junction 3, the ground reference is to be considered as a short-circuited stub.

Where $Z_{inJnL}$ is the input impedance of the next stage in the manifold network, $Z_{inJnU}$ is the input impedance looking into junction n, and $Z_{in#n}$ is the input impedance of sub-band n looking from the junction. Moreover, $\beta$ is the phase constant ($=2\pi/\lambda$), and $d_{tn}$ are the horizontal transmission-lines.

\[
Z_{inJnL} = jZ_0 \tan(\beta d_{m3})
\]

\[
Z_{inJnU} = Z_{inJ3L} / Z_{in#3}
\]

\[
Z_{inJ2L} = Z_0 \frac{Z_{inJ3U} + jZ_0 \tan(\beta d_{m2})}{Z_0 + jZ_{inJ3U} \tan(\beta d_{m2})}
\]

\[
Z_{inJ2U} = Z_{inJ2L} / Z_{in#2}
\]

\[
Z_{inJ1L} = Z_0 \frac{Z_{inJ2L} + jZ_0 \tan(\beta d_{m1})}{Z_0 + jZ_{inJ2L} \tan(\beta d_{m1})}
\]

\[
Z_{inJ1U} = Z_{inJ1L} / Z_{in#1}
\]

From the respective junction looking into each sub-band we get, where $Z_{in#i}$ is the input impedance of the respective sub-bands and $Z_{inbpn}$ is input impedance of the filter at the sub-band:

\[
Z_{in#1} = Z_0 \frac{Z_{inbp1} + jZ_0 \tan(\beta d_{l1})}{Z_0 + jZ_{inbp1} \tan(\beta d_{l1})}
\]

\[
Z_{in#2} = Z_0 \frac{Z_{inbp2} + jZ_0 \tan(\beta d_{l2})}{Z_0 + jZ_{inbp2} \tan(\beta d_{l2})}
\]

\[
Z_{in#3} = Z_0 \frac{Z_{inbp3} + jZ_0 \tan(\beta d_{l3})}{Z_0 + jZ_{inbp3} \tan(\beta d_{l3})}
\]

The broadside- and edge-coupled filters are built up with coupled line sections, each with unique even- and odd-mode impedances, $Z_{0e}$ and $Z_{0o}$, respectively are cascaded to create the band-pass filter. The geometrical design of each edge and broadside coupled segments determine their respective odd and even mode impedance. The combination of even and odd mode determines the characteristics of each section. Even and odd mode sections can be exchanged but due to their respective physical build-up, broadside coupled sections are suitable for sections that requires relatively strong coupling and vice versa [23]. Hence, two closely spaced metal surfaces on top of each other exhibits a strong coupling, low impedance in-between (Lower odd mode impedance compared to an edge coupled section). Fig. 3 in the previous section shows an illustration of a band-pass filter with five coupled sections. The input impedance of a section ($Z_{incl}$) is defined as [41]:

\[
Z_{incl} = \frac{1}{2 \sin(\beta l)} \sqrt{(Z_{0e} - Z_{0o})^2 - (Z_{0e} + Z_{0o})^2 \cos^2(\beta l)}
\]

From the respective junction looking into each sub-band we get, where $Z_{in#i}$ is the input impedance of the respective sub-bands and $Z_{inbpn}$ is input impedance of the filter at the sub-band:

\[
Z_{inJnL} = jZ_0 \tan(\beta d_{m3})
\]

\[
Z_{inJnU} = Z_{inJ3L} / Z_{in#3}
\]

\[
Z_{inJ2L} = Z_0 \frac{Z_{inJ3U} + jZ_0 \tan(\beta d_{m2})}{Z_0 + jZ_{inJ3U} \tan(\beta d_{m2})}
\]

\[
Z_{inJ2U} = Z_{inJ2L} / Z_{in#2}
\]

\[
Z_{inJ1L} = Z_0 \frac{Z_{inJ2L} + jZ_0 \tan(\beta d_{m1})}{Z_0 + jZ_{inJ2L} \tan(\beta d_{m1})}
\]

\[
Z_{inJ1U} = Z_{inJ1L} / Z_{in#1}
\]

From the respective junction looking into each sub-band we get, where $Z_{in#i}$ is the input impedance of the respective sub-bands and $Z_{inbpn}$ is input impedance of the filter at the sub-band:

\[
Z_{in#1} = Z_0 \frac{Z_{inbp1} + jZ_0 \tan(\beta d_{l1})}{Z_0 + jZ_{inbp1} \tan(\beta d_{l1})}
\]

\[
Z_{in#2} = Z_0 \frac{Z_{inbp2} + jZ_0 \tan(\beta d_{l2})}{Z_0 + jZ_{inbp2} \tan(\beta d_{l2})}
\]

\[
Z_{in#3} = Z_0 \frac{Z_{inbp3} + jZ_0 \tan(\beta d_{l3})}{Z_0 + jZ_{inbp3} \tan(\beta d_{l3})}
\]

The broadside- and edge-coupled filters are built up with...
The vertical transmission-lines in the manifold provide a high input impedance towards the next manifold stage ($Z_{inL}$) in the passband of the sub-band at the respective junction. Similarly, the horizontal “T-line for stop-band tuning” is to provide a high sub-band input impedance ($Z_{in#n}$) in the stop-band frequencies close to the sub-band. The solid curves in Fig. 5(a) and (b) show typical input impedance responses of a coupled line filter. It is shown that the stop-band near the passband is close to short, i.e., a direct connect to the junction would severely affect the neighboring sub-bands according to equation (2)-(6). The dashed curve shows the input impedance when a microstrip-line with the length ($d_{m2}$) is added. With the additional electrical length the stop-band impedance response is rotated to a high value, i.e., approximately equal to an electrical open in neighboring passband frequencies.

Fig. 6 shows how forward transmission of a sub-band (sub-band #1 in this example) and the common port input impedance is affected by the stop-band impedances of neighboring sub-bands. The calculations in Fig. 6a are performed using the following assumptions: the total vertical transmission line ($d_{m1}+d_{m2}+d_{m3}$) is assumed to be $\lambda/4$, $Z_{in3} >> Z_{in1}$, and $Z_0=50 \, \Omega$. Furthermore, the filters and all transmission lines are assumed to be loss-less. It is seen that a low input impedance of a neighboring sub-band has severe impact on the multiplexer performance, and when the stop band impedance increases the impact reduces quickly. The calculations shown in Fig. 6(b) were done using the same assumptions as in 6(a), but it also includes the impact of non-ideal $Z_{in3}$ (Impact of sub-band #3 on sub-band #1’s forward transmission). Similarly as in Fig. 6(a) it is seen that when the stop-band impedance increases the impact reduces quickly. Moreover, note that the lines when $Z_{in3} = 600, 800,$ and $1000 \, \Omega$ are hard to distinguish due to the small difference.

IV. SIMULATED AND MEASURED RESULTS

Design and simulation were done with Advanced Design System (ADS) 2009 from Agilent technologies Inc. Measurements were done with a Rhode&Schwartz ZVM vector network analyzer.

A. Triplexer

Fig. 7(a) shows the simulation and measurement results of forward transmission of the triplexer. A flat, ripple-free passband response is seen. The transmission line network is optimized together with the filters to achieve high blocking of neighbouring bands. A good match between simulation and measurement results is seen. The complete flex-rigid multi-layer printed circuit board was simulated with radiation characteristics enabled in ADS for the best accuracy. However, some limitations still exist in the simulations. The SMA connectors are not included, and ADS Momentum does not take surface roughness into count. The simulated insertion loss is 3.35-3.85 dB, and measured insertion loss is 3.41-4.10 dB for the three sub-bands. As a comparison it is worth mentioning that simulation of stand alone filters indicated an
insertion loss between 2.34-2.49 dB (Only filter). All sub-bands have at least 1 GHz bandwidth at -3 dB from the top. Fig. 7(b) and (c) show the simulation and measurement results of return loss of the triplexer expressed with S-parameters. Smooth forward transmission and neighbouring band blocking were prioritized over low return loss, as seen in Fig. 7(a)-(c). The only really noticeable difference is a small frequency shift, due to the fact that the simulated phase velocity differs from the actual, which is a known fact for this substrate stack [4], [23] and [36]. Because of this well known difference a scaled version of the triplexer was also designed, fabricated and measured, see the dotted line labelled -5 % in Fig. 7(a). The -5 % notation is with respect to target frequency, i.e., all electrical lengths were scaled up with 5 %. Note that the -5 % (dotted lines) overlap a lot with the simulated results (solid lines). The design freedom to achieve a low return loss and smooth forward transmission is therefore limited. Comparing simulated and measured input reflection it is seen that the curves have the same shape but some narrowband variations exist. This is likely due to the fact that the SMA connectors were not included in the simulations [23]. Fig. 7(d) and (e) show isolation between Ports 2-4. It is seen that the minimum isolation is 15 dB. The minimum isolation occurs at the boundary of the neighbouring sub-bands, so in the three passbands the isolation is better than 15 dB. The isolation between the non-neighbouring alternate sub-bands is 45 dB. Fig. 7(f) and (g) show simulated and measured phase response, respectively. It is seen that all three sub-bands have a high degree of linearity within their respective passbands. Fig. 7(h) and (i) show simulated and measured group delay, respectively. It is seen that for all the 1 GHz sub-bands the group delay is around 1.0 ns and the variation is approximately 0.4 ns within each sub-band. It is the filter type and filter order that dominate the delay [4] and [23].
V. DISCUSSION

A good agreement with only minor deviations was observed between simulated and measured results. For instance, measured insertion loss was approximately 0.2 dB more than the simulated one. A slightly upward shift in frequency is also seen, i.e., a constant error of 5 % which is typical known deviation for this substrate [37]. The frequency shift is due to the fact that the simulated electrical length of the distributed circuitry differs from the measured one, i.e., the simulated phase velocity is lower than the measured one. This is proven by the additional “-5 %” scaled design, see in Fig. 7(a). The higher insertion loss from the measurements can be explained by the surface roughness that is not included in the simulations because Momentum in ADS does not support that. It is a known fact that surface roughness increases insertion loss [23], [42], [43]. Actually surface roughness could in the worst case add even more than the difference seen here, roughly up to half a dB at the line-lengths used here at 6-9 GHz [23]. Moreover, some additional insertion-loss may come from reflections caused by the SMA connectors soldered on the triplexer [23].

The group delay variation was measured to be about 0.4 ns within each sub-band. How this impact on a system performance depends of the implementation and a simple conclusion cannot be directly drawn. However, in pulsed systems a small group delay variation might already cause degradation of system performance, while in orthogonal frequency division multiplexing systems group delay impact on the system level performance is limited [28].

By using broadside coupled microstrips, the printed circuit board tolerance requirement on the minimum spacing for the whole triplexer is relieved. Thus, this triplexer can be integrated into a flex-rigid four metal layer printed circuit board without any particular requirements on the fabrication process. This opens way for full integration of the radio frontend on flex-rigid printed circuit boards, including a flexible antenna in the future. Hence, compared to previously known works the flex rigid printed circuit board technology can host distributed circuitry with similar performance as conventional high frequency printed circuit boards [14], [18], [23], [44].

VI. CONCLUSION

A fully printed circuit board integrated planar triplexer using microstrips for three UWB bands within the 6-9 GHz frequency band has been studied and presented. The theoretical analysis shows that high stop-band input impedance in adjacent channels is crucial for a high performance (e.g., low insertion loss) when utilizing the manifold multiplexing principle. A demonstrator on a flex-rigid printed circuit board for the 6-9 GHz frequency band was designed and evaluated. Three flat 1-GHz sub-bands have been achieved. The triplexer can be integrated into a multilayer flex-rigid printed circuit board using a commercial process technique at low cost, even though the sub-bands are
side by side without any guard-band in between. A phase response with a high degree of linearity and a small group delay variation of 0.4 ns are observed for all the three sub-bands. Finally, detailed modeling of the flexible, rigid, and adhesion layers provided good simulation prediction of the prototype over all performance, e.g., see Fig. 1 and 7(a).

REFERENCES

Magnus Karlsson was born in Västervik, Sweden in 1977. He received his M.Sc., Licentiate of Engineering and Ph.D. degrees from Linköping University in Sweden, in 2002, 2005 and 2008, respectively. In 2003 he started in the Communication Electronics research group at Linköping University and is currently working as a senior researcher. His main work involves wideband antenna-techniques, wideband transceiver front-ends, and wireless communications.

Adriana Serban received an M.Sc degree in electronic engineering from Politehnica University, Bucharest, Romania. From 1981 to 1990 she was with Microelectronica Institute, Bucharest as a Principal Engineer and was involved in mixed integrated circuit design. From 1992 to 2002 she was with Siemens AG, Munich, Germany and with Sicon AB, Linköping, Sweden as analog and mixed signal integrated circuit Senior Design Engineer. Since 2002 she has been a Lecturer at Linköping University teaching in analog/digital system design and RF circuit design. She is recevied her Ph.D degree in Communication Electronics 2010. Her main research interest has been RF circuit design and high-speed integrated circuit design.

Joakim Östh received his M.Sc. degree in 2006 from Linköping University, Sweden. Since 2009 he has been a Ph.D. degree in communication electronics at Linköping University, Sweden. His main research interest has been communication electronics including RF design, wireless communications and high-speed data transmissions.

Owais received his B.E. degree in 1996 and Master degree in 2000 from University of Engineering and Technology Peshawar Pakistan. He is employed as assistant professor at Comsats Institute of Information Technology, Abbotabad, Pakistan. Since 2007 he has been PhD student in communication electronics at Linköping University, Sweden. His main research interest has been Antenna design and high-speed data transmissions.

Shaofang Gong was born in Shanghai, China, in 1960. He received his B.Sc. degree from Fudan University in Shanghai in 1982, and the Licentiate of Engineering and Ph.D. degrees from Linköping University in Sweden, in 1988 and 1990, respectively. Between 1991 and 1999 he was a senior researcher at the microelectronic institute – Acreo in Sweden. From 2000 to 2001 he was the CTO at a spin-off company from the institute. Since 2002 he has been full professor in communication electronics at Linköping University, Sweden. His main research interest has been communication electronics including RF design, wireless communications and high-speed data transmissions.